

## EAST Search History

| Ref # | Hits | Search Query   | DBs   | Default Operator | Plurals | Time Stamp       |
|-------|------|--|---|------------------|---------|------------------|
| L1    | 1    | ((non adj volatile adj memory) and interface and (pseudo adj parallel adj communication) and (wafer adj sort adj test)).clm. | US-PGPUB;<br>USPAT;<br>USOCR;<br>EPO; JPO;<br>DERWENT;<br>IBM_TDB | OR               | OFF     | 2006/07/22 12:56 |
| L2    | 137  | polizzi.inv.   | US-PGPUB;<br>USPAT;<br>USOCR;<br>EPO; JPO;<br>DERWENT;<br>IBM_TDB | OR               | OFF     | 2006/07/22 12:56 |
| L3    | 49   | perroni.inv.   | US-PGPUB;<br>USPAT;<br>USOCR;<br>EPO; JPO;<br>DERWENT;<br>IBM_TDB | OR               | OFF     | 2006/07/22 12:56 |
| L4    | 33   | 2 and 3  | US-PGPUB;<br>USPAT;<br>USOCR;<br>EPO; JPO;<br>DERWENT;<br>IBM_TDB | OR               | OFF     | 2006/07/22 12:56 |
| L5    | 133  | schillaci.inv.   | US-PGPUB;<br>USPAT;<br>USOCR;<br>EPO; JPO;<br>DERWENT;<br>IBM_TDB | OR               | OFF     | 2006/07/22 12:57 |
| L6    | 3    | 4 and 5  | US-PGPUB;<br>USPAT;<br>USOCR;<br>EPO; JPO;<br>DERWENT;<br>IBM_TDB | OR               | OFF     | 2006/07/22 12:57 |